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L17	0
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US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index

Database: **IBM Technical Disclosure Bulletins**

L18

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Search History

DATE: **Tuesday, October 15, 2002** [Printable Copy](#) [Create Case](#)

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side		result set	
	<i>DB=TDBD; PLUR=YES; OP=OR</i>		
<u>L18</u>	L17	0	<u>L18</u>
	<i>DB=DWPI; PLUR=YES; OP=OR</i>		
<u>L17</u>	L16	0	<u>L17</u>
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<u>L16</u>	L15	0	<u>L16</u>
	<i>DB=JPAB; PLUR=YES; OP=OR</i>		
<u>L15</u>	L14	0	<u>L15</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L14</u>	L13	0	<u>L14</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L13</u>	L12	0	<u>L13</u>
	<i>DB=TDBD; PLUR=YES; OP=OR</i>		
<u>L12</u>	L11 and "common source"	0	<u>L12</u>
<u>L11</u>	L10 and ("plural drains" OR "plural drain regions" OR "drain regions")	9	<u>L11</u>
<u>L10</u>	"floating gates"	80	<u>L10</u>
<u>L9</u>	("Floating gate" near8 drain) and ("floating gate" near7 source) and "common source" and 11	0	<u>L9</u>
<u>L8</u>	L7	0	<u>L8</u>
	<i>DB=DWPI; PLUR=YES; OP=OR</i>		
<u>L7</u>	L6	0	<u>L7</u>
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<u>L6</u>	L5	0	<u>L6</u>
	<i>DB=JPAB; PLUR=YES; OP=OR</i>		
<u>L5</u>	L4	0	<u>L5</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L4</u>	L3	0	<u>L4</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L3</u>	("Floating gate" near4 drain) and ("floating gate" near4 source) and "common source" and 11	0	<u>L3</u>
<u>L2</u>	L1 and "floating gates"	3546	<u>L2</u>
<u>L1</u>	"flash memory"	12773	<u>L1</u>

END OF SEARCH HISTORY